

ABOUT THE INSTITUTION

AAA college of Engineering & Technology, (AACET) Sivakasi is one among the renowned colleges for technical education in the state of Tamilnadu. AACET is NAAC accredited with A grade and accredited by NBA (CSE, ECE, EEE and MECH) and is branded for its quality education, promoted by Sony Vinayaga group of industries. Aspire, Attain, Achieve are its motto. The primary aim of our college is to provide quality education. The college is managed by Panjurajan- Amaravathy trust located at Thiruthangal, Virudhunagar District. Our college has received the Best Infrastructure Award from Dr. K. Rosaiah, the former Governor of Tamilnadu. The college has signed MoU's with leading industries and has established Centre of excellences in Industrial Internet of Things (IIoT), BOSCH – Centre for car, e-Yantra Robotics Lab, Amazon Web services, Oracle Workforce Development and Texas Instruments Innovation Lab .

ABOUT THE DEPARTMENT

Department of Electronics and Communication Engineering was established in the year 2013 with an aim of creating quality electronics and communication engineers with sound technical knowledge in emerging technologies. It has consistently achieved excellent results in the university examinations and have obtained university rank in the year 2018. The department has Centre of Excellence in "Industrial Internet of things (IIoT)", e-Yantra Robotics Laboratory and Texas Instruments Innovation Lab. The department has well established IETE Students forum through which various technical activities are conducted every year. Department has state of the art Laboratories such as Advanced communication lab, Microprocessor lab, Electronics lab, Optical and Microwave lab, Digital/Analog circuit lab and VLSI lab. The department has MOU with BSNL & various other industries, to provide real time training for the students in the field of advanced communications. Students are given exposure to industries by periodically arranging guest lectures by industrial experts and industrial visits.

ORGANIZING COMMITTEE

PATRON

Dr. P. Karvannan, Secretary
Dr. P. Ganesan, Correspondent
Dr. K. Vignesh Kumar, Joint Secretary

PRESIDENT

Dr. M. Sekar, M.E., Ph.D., FIE., Principal,

CONVENOR

Dr. T. Senthil, M.E., Ph.D
Professor & HOD / ECE, AACET

EVENT COORDINATORS

Dr. B. Padmanaban, M.E., Ph.D,
Associate Professor/ECE
Mrs. K. Padmapriya, M.E., (Ph.D)
Assistant Professor/ECE

ADDRESS FOR COMMUNICATION

Dr. B. Padmanaban, ASP/ECE
AAA College of Engineering and Technology,
Amathur, Sivakasi-626005
Email: padmanaban@aacet.ac.in
Phone : 99941 63733

Web: <https://www.aaaengcoll.ac.in/faculty-development-program-on-recent-trends-and-advancements-in-semiconductor-devices-modelling-through-machine-learning-from-07-12-2023/>



COLLEGE OF ENGINEERING AND TECHNOLOGY, SIVAKASI.
(Approved by AICTE, New Delhi & Affiliated to Anna University, Chennai)
(Accredited by NBA [CSE, ECE, EEE, MECH])
(Accredited by NAAC with 'A' grade, An ISO TUV 21001 : 2018 Certified Institution)
Kamarajar Educational Road, Amathur, Sivakasi - 626 005.



**Three-day Faculty
Development Programme On**

**Recent Trends and Advancements
in Semiconductor Devices Modelling
Through Machine Learning**

23.01.2024 - 25.01.2024

Organized by
**Department of
Electronics and Communication Engineering**
in association with
IETE STUDENTS CHAPTER

Website: www.aaaengcoll.ac.in

TARGET AUDIENCE

Faculty members of AICTE approved Institutions, Universities, Polytechnic Colleges, Research scholars, PG Scholars, Engineers from R & D Labs and Government Sectors.

REGISTRATION FEE

- Registration fee: Rs. 500/-
- Registration fee may be paid through Demand Draft. DD should be drawn in favour of "AAA College of Engineering and Technology" payable at Sivakasi.
- As the numbers of seats are limited registration will be confirmed on first come first serve basis.
- Certificates will be provided to all the participants.

IMPORTANT DATES

Starting date for registration	07.01.2024
Last date for receipt of application	20.01.2024
Intimation of selection	22.01.2024

E-mail the scanned copies of filled-in and duly signed application form along with DD to

padmanaban@aacet.ac.in

For more enquiries please Contact:

Dr. B. PADMANABAN, ASP/ECE : 9994163733

Mrs. K. PADMAPRIYA, AP/ECE : 8531822334

ABOUT THE FDP

The FDP mainly focuses on Emerging VLSI technology and Nano Scale Device Modelling. Machine Learning algorithms used in Nano scale semiconductor device design. It also aims to impart the knowledge on transistor scaling, device modelling, schematic and layout using EDA tools. Further, this FDP will help the participants to sustain research in the area of VLSI devices and provide an idea about a path to extend the research findings to the real time industrial applications

TOPICS COVERED

- Semiconductor Device Modelling
- Electronic Design Automation
- Machine Learning
- Quantum Device Modelling and ML Techniques
- Real-time Analysis and ML Deployment in EDA

RESOURCE PERSONS

Dr. N.B Balamurugan,
Professor/Department of ECE
Thiyagarajar College of Engineering, Madurai.

Dr. R. Ramesh,
Professor/Department of ECE
Sastra University, Thanjavur.

Dr. V. Sandeep,
Post-Doctoral Researcher,
Kalasalingam University, Krishnankovil.

Dr. N. Pothirasan,
Proprietor, Hashan Medicare,
Bangalore.

AAA COLLEGE OF ENGINEERING AND TECHNOLOGY

Department of
Electronics and Communication Engineering
**THREE-DAY FACULTY DEVELOPMENT
PROGRAMME On**

Recent Trends and Advancements in Semiconductor Devices Modelling Through Machine Learning

23.01.2024 to 25.01.2024

REGISTRATION FORM

Name :

Designation :

Organization :

Address :

E-Mail :

Mobile No :

Accommodation Required: YES / NO

Payment Details :

DD Number :

Date :/...../2024

Bank Name :

Branch :

Signature of the
HoD / Head of the Institution

Signature of the
Applicant